

L1: (3) *628537*
L13: (9626) pc adj3 card
L20: (840) flash adj3 chips!
L21: (26) 13 same 20
L26: (94) 13 and 20
L27: (68) 26 not 21
L34: (7423) flash adj3 card
L35: (71) 20 same 34
L36: (49) 35 not 26
L39: (148) 20 and 34
L40: (87) 39 not 26
L41: (38) 40 not 35
L42: (74890) (ic or memory or 77prom) adj3 card
L43: (390) 77prom adj3 chips!
L44: (327) 20 and 42
L46: (146) 20 same 42
L47: (66) 46 not 39
L49: (146) 42 and 43
L50: (122) 49 not 46
L51: (114) 50 not 39
L52: (451) (WALLACE near2 ROBERT).in.
L53: (440) (NORMAN near2 ROBERT).in.
L54: (156) (HARARI near2 ELIYAHOU).in.
L55: (208284) 365/
L58: (1108) 365/52
L59: (2821) 365/63
L60: (2104) 365/185.33
L61: (81038) 13 or 42
L62: (283) 61 and 58
L63: (181) 61 and 59
L64: (283) 61 and 60
L65: (2806) 61 and 55
L66: (166) 65 and 20
L67: (76) 65 and 43
L86: (1085) 20 or 43
L87: (214) 61 same 86

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|---|---|---|-------|---|
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| Default operator: | | <input type="text" value="OR"/> <input checked="" type="checkbox"/> Highlight all hit terms initially | | |
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| 1 | <input checked="" type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | US 4982378 A | 19910101 | 11 | 365/189.01 | 365/201; | Matsushita, Tsuyoshi | Memory capacity detecting devi | <input type="checkbox"/> |